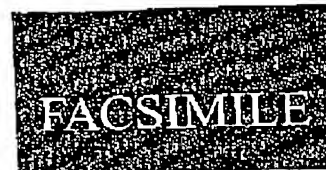


OCT 25 2005

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Date October 25, 2005

Time

Pages including

File no. Our Ref: XA-9597

cover

10

Re: U.S. Patent Appln. No. 10/033,792

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OCT 25 2005

XA-9597
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Ikuya OONO et al.

Appln. No.: 10/033,792

Group Art Unit: 2681

Filed: January 3, 2002

Examiner: W. Cai

For: SIGNAL PROCESSING SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
AND WIRELESS COMMUNICATION SYSTEM

AMENDMENT PRELIMINARY TO CONTINUED EXAMINATION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Prior to the commencement of continued examination,
please amend the above-identified patent application as
indicated below.

Amendments to the Claims are reflected in the listing of
claims which begins on page 2 of this paper.

Remarks begin on page 9 of this paper.